About Anna University Chennai

Anna University was established on 4th September 1978 as unitary type of University. Its constituent colleges are College of Engineering, Madras Institute of Technology, Alagappa College of Technology and School of Architecture and Planning. It offers higher education in Engineering, Technology and allied Sciences that are relevant to the present needs of the society. Anna University has a strong vision to impart excellence in education, research and societal service through highly dedicated and competent teachers who are also expert researchers in various emerging and cutting edge technologies. Besides promoting research and disseminating knowledge gained there from, it fosters cooperation between the academic and industrial communities.

About Madras Institute of Technology

Madras Institute of Technology is a pioneer institute established by the great visionary and industrialist Late Shri C. Rajam on 18th of July 1949 and it merged with Anna University in the year 1978. The educational practice of this institution is application oriented, thereby helping the students make a smooth transition from college to the industrial world. This is reflected in the profile of vast number of MIT alumini occupying key positions in various MNCs and Research Organizations across the globe. Shri Dr. APJ Abdul Kalam, our former president of India, is an alumnus of MIT.

About Electronics Engineering

The Department of Electronics Engineering established in the year 1949 has its core strength in the area of Electronics & Communication technology. The cutting edge research areas include Communication Technologies, Wireless Communication, Signal Processing, Image Processing & Pattern Recognition, Network Security, Sensor Networks, Optical Communication and VLSI. The department is supported by DST - FIST & UGC –DRS-SAP Phase II program.

About the Workshop

Demand for qualified VLSI engineers is exponentially increasing over the years. The academic community is unable to meet the demands of semiconductor industry. Exorbitant cost of EDA tools, online support for self learning, non availability of expert teaching assistance and complexities in tool usage have restricted academic community to impart relevant training to deserving candidates to pursue their career in VLSI design. Top EDA companies such as Cadence, Synopsys and Mentor Graphics have supported academic institutions through their University Programs, however, some institutions have not be able to significantly benefit due to several reasons. There are good numbers of open source EDA tools that are on par with commercial EDA tools for VLSI design. Very few in India have attempted to use them, top universities in the US, UK, Australia, Germany and France use open source EDA tools to teach undergraduate and post graduate courses.

Organizing Committee

Chairman
Dr S Ganesan
Registrar, Anna University, Chennai

Co-Chairman
Dr A Rajadurai
Dean, MIT Campus

Co-ordinator
Dr M Kannan
Associate Professor & Head,
Dept. of Electronics Engineering,
MIT Campus, Anna University

Co-coordinator
Mr E Konguvel
Teaching Fellow,
Dept. of Electronics Engineering,
MIT Campus, Anna University

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Important Dates

Last Date of Receipt of application with DD	:	20th March 2017
Intimation of Selection (Only through E-Mail)	:	23rd March 2017

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In association with

Sanaris Technologies

SANARIS TECHNOLOGIES
Bengaluru, Karnataka – 560032.

Registration

Course Fee

Academicians/Industry delegates: Rs. 1000/-
Researchers/UG/PG Students: Rs. 750/-

Registration will be done on "First Come First Served" basis. As seats are limited to 30, early registration is recommended. Registration shall be done only by using the following **link** or by **scanning** the QR code:

CLICK HERE
For Online
Registration

Interested aspirants shall send (through post) the printed copy of filled-in registration response (the E-mail you receive after online registration) & Demand Draft* in favour of **"The Dean, MIT Campus"** payable at Chennai to the following address:

The Co-ordinator,
Two Days Workshop on
"VLSI Design using Open Source EDA Tool – AARON"
Department of Electronics Engineering,
MIT Campus, Anna University,
Chromepet, Chennai – 600044.

E-Mail: vlsielex17@gmail.com
Phone: +91 8015823900

*Kindly write your name & institution name at the back side of your Demand Draft.

Target Audience

- ✓ Industry delegates
- ✓ Academicians
- ✓ Research Scholars
- ✓ PG & UG Students

Course Contents

- ✓ ASIC design flow using open source platform
- ✓ Analog IC design – Schematic Capture, Layout design & Optimization
- ✓ RTL to GDS II design flow using open source EDA tool

Course Objectives

- ✓ To demonstrate low-cost alternative to the proprietary software.
- ✓ To create awareness among academic community about the availability of open source EDA tools for carrying out RTL – GDS II flow.
- ✓ To demonstrate ASIC design flow from RTL – GDS II flow using open source EDA tools.
- ✓ To demonstrate design of analog and mixed signal circuits using open source EDA tools.

For more details:
www.annauniv.edu
www.mitindia.edu

Two Days Workshop on
"VLSI Design using Open Source EDA Tool – AARON"

31st March & 01st April 2017

Department of Electronics Engineering,
Madras Institute of Technology Campus,
Anna University, Chennai – 600044.